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CONFIRMATION NO. 5605

## CORRECTED FILING RECEIPT



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Thomas M Coester  
 Blakely Sokoloff Taylor and Zafman LLP  
 12400 Wilshire Boulevard Seventh Floor  
 Los Angeles, CA 90025

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## Applicant(s)

Michael Chow, Cupertino, CA;  
 Elango Ganesan, Seiad Valley, CA;  
 John William Phillips, Menlo Park, CA;  
 Nazar Abbas Zaidi, San Jose, CA;

Domestic Priority data as claimed by applicant

Foreign Applications

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Non-Publication Request: No

Early Publication Request: No

## Title

Method and apparatus for achieving architectural correctness in a multi-mode processor providing floating-point support

**Preliminary Class**

712

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Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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